REMARKS

The indication of the allowability of Claim 7, now written in independent form so as to include the features of original Claims 1 and 6, is noted with appreciation.

The objection to the drawings is addressed by the submission of replacement pages containing Figs. 8 and 13 with the label "PRIOR ART".

The rejection of Claims 1, 3 and 10 have been addressed or rendered moot by the foregoing amendments. Reconsideration is respectfully requested.

Applicants submit, however, that Claim 1 is not unclear as to which signal prompts the task processor. The original claim clearly states that the interruption signal is the basis for the task processor to perform the task process. However, for subsequent interruption signals, as described in the Specification, the task process is performed on the basis of the task processing signal from the polling unit. See, for example, page 6, line 19 to page 7, line 5 of the Specification. Reconsideration is thus requested.

Clarification of the assertion that the IDS filed January 30, 2006, fails to comply with 37 C.F.R. §1.98(a)(2) is requested as to which document and why it allegedly fails to comply. To the extent that the Office Action is referring to the English-language article entitled "Operating Systems Concepts," Applicants

note that this publication is discussed at pages 2 and 3 of their Specification. The Examiner is requested to clarify her refusal to consider a document discussed in the Specification and written in the English language. Nothing in Patent and Trademark Office rules requires Applicant to explain why they submitted the document although their Specification does so.

The rejection of Claims 1-6 and 8-14 as being anticipated by Maupin under 35 U.S.C. §102(b) is traversed, and reconsideration is respectfully requested.

Claim 1 now incorporates the feature of Claim 2. To paraphrase what is now in amended Claim 1, Applicants state that their invention has two basic features, namely, (a) task deciding means for deciding continuity of the current event process, and (b) event processing means that when said decision result is continuation, continuously executes current event process.

In other words, the claimed system decides if the same task is repeated (continuation), the event processor continuously executes the same task.

The inventive concept is illustrated by reference to the timing charts of Figs. 8 and 9, the former being a conventional event cyclic task and the latter showing the invention which eliminates the useless overhead time Ti and thus a delay of processing start time resulting from an accumulation of overhead times.

Thereby, the system of the present invention allows quicker and more reliable response to a plurality of instructions.

The Office Action references col. 7, lines 20-26, lines 36-38 and lines 63-66 of the Maupin patent. Applicants submit that, contrary to how these referenced sections of Maupin are characterized in the Office Action, they teach nothing more that memorization of a plurality of different tasks, ("next task") by the schedule list and sequential execution of these different tasks until all tasks are executed. Overhead time is still required at the execution of the different tasks. That is, Maupin is silent about either repeated performance of the same task, or shortening or eliminating overhead time via a task processor that decides continuity of a current event process and continues execution of the same event process if continuation is decided.

Accordingly, early and favorable action is earnestly solicited.

If there are any questions regarding this amendment or the application in general, a telephone call to the undersigned would be appreciated since this should expedite the prosecution of the application for all concerned.

If necessary to effect a timely response, this paper should be considered as a petition for an Extension of Time sufficient to effect a timely response, and

please charge any deficiency in fees or credit any overpayments to Deposit Account No. 05-1323 (Docket #056208.57315US).

Respectfully submitted,

September 10, 2007

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FIG. 8 PRIOR ART

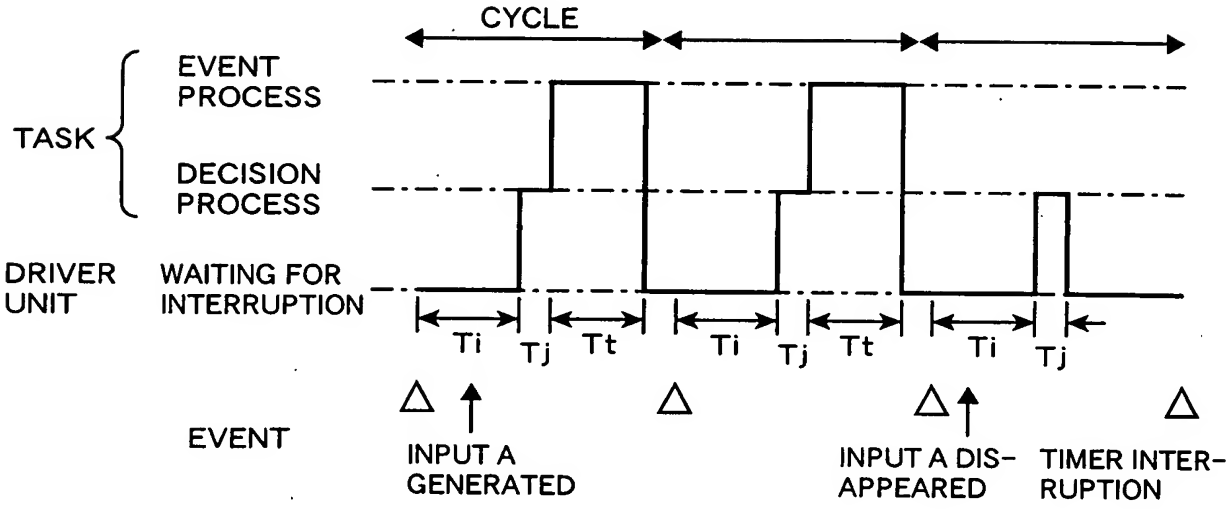

FIG. 9

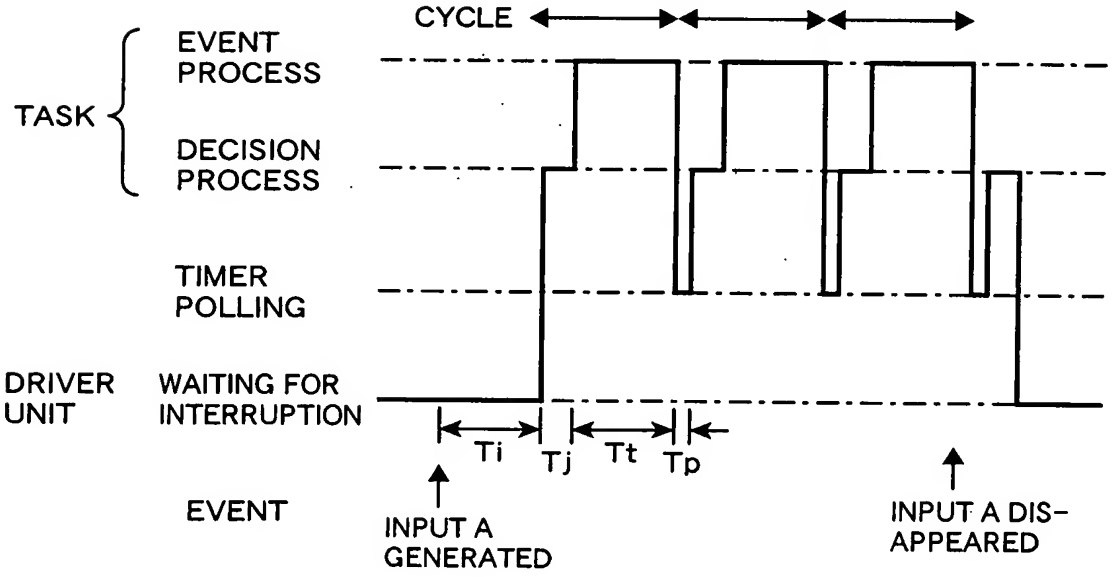

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FIG. 12

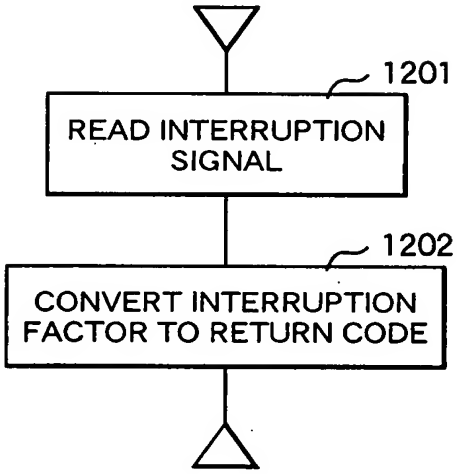
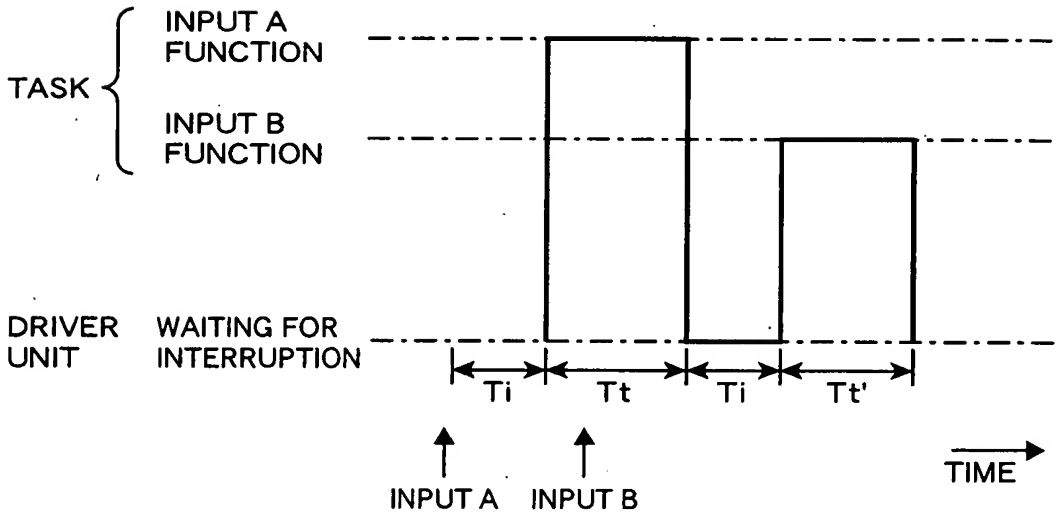
FIG. 13 PRIOR ART